

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	546	(stor\$3 sav\$3 transmit\$4) with ((initial begin\$4) adj2 address) with ((initial begin\$4) adj2 address)	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/29 11:37
L2	3380	(stor\$3 sav\$3 transmit\$4) with ((initial begin\$4) adj2 address) with ((initial begin\$4) adj2 address)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/29 11:37
L3	5	bootstrap same 2	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/29 11:37
L4	0	1 and bootstrap	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/29 11:37
L5	29	((bootstrap with (eeprom or eeprom or flash)) and g06f\$.ipc.) and "713"/\$.ccls.	USPAT	OR	OFF	2005/07/29 11:59
L6	28	((bootstrap with (eeprom or eeprom or flash)) and g06f\$.ipc.)	EPO; JPO; DERWENT	OR	OFF	2005/07/29 11:59
L7	1	(bootstrap near4 (bios program)) with ((prior before) near6 (cpu processor))	USPAT	OR	OFF	2005/07/29 12:07
L8	0	(bootstrap near4 (bios program)) with ((prior before) near6 (cpu processor))	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/29 12:07
L9	116	(bios boot bootstrap) with ((prior before) near6 (cpu processor))	USPAT	OR	OFF	2005/07/29 12:37
L10	14	(bios boot bootstrap) with ((prior before) near6 (cpu processor))	EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/29 12:07
L11	305	(binary near1 control) near9 bit\$1	USPAT	OR	OFF	2005/07/29 12:37
L12	0	9 and 11	USPAT	OR	OFF	2005/07/29 12:37